

FIG. 1

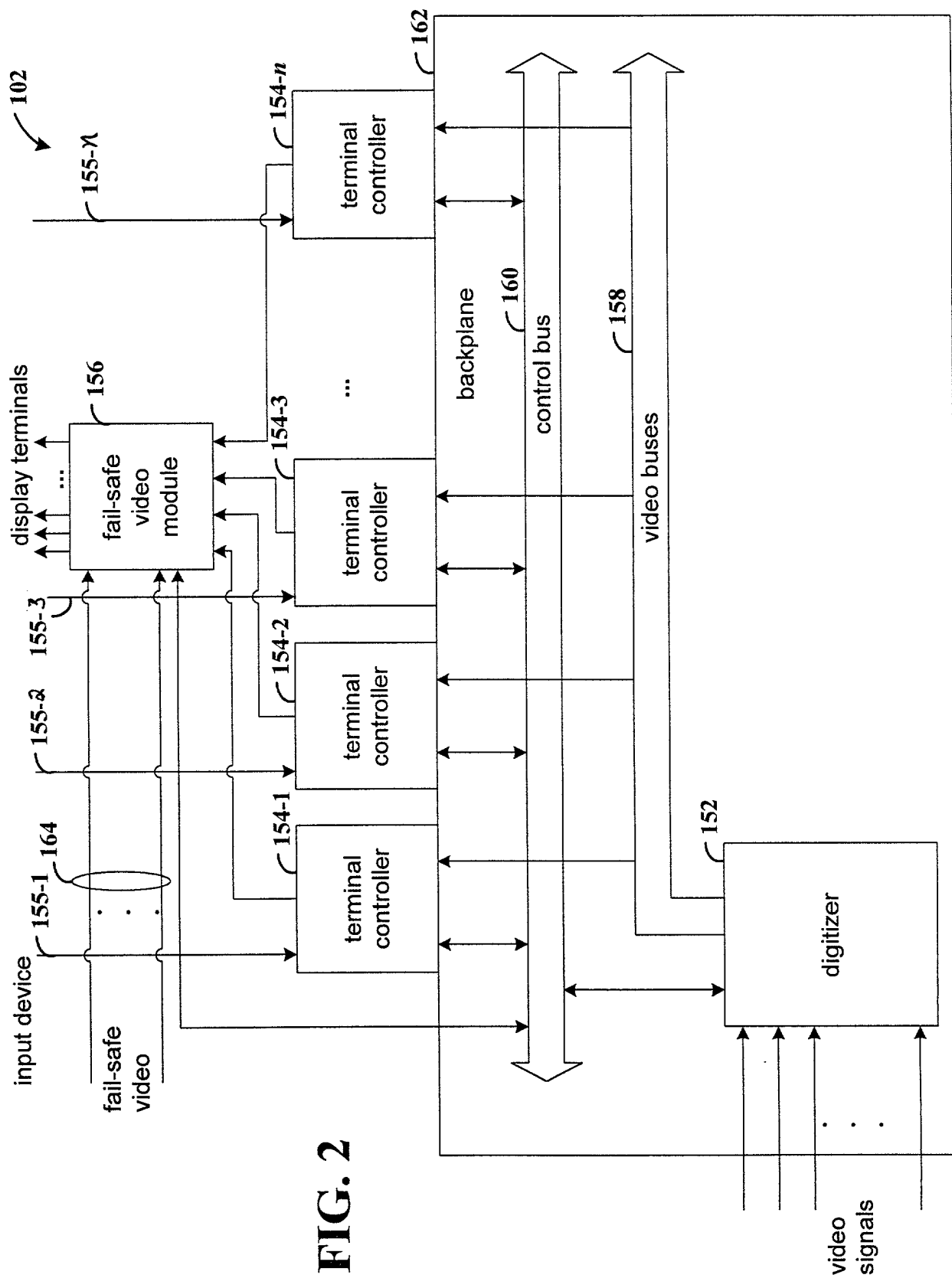


FIG. 2

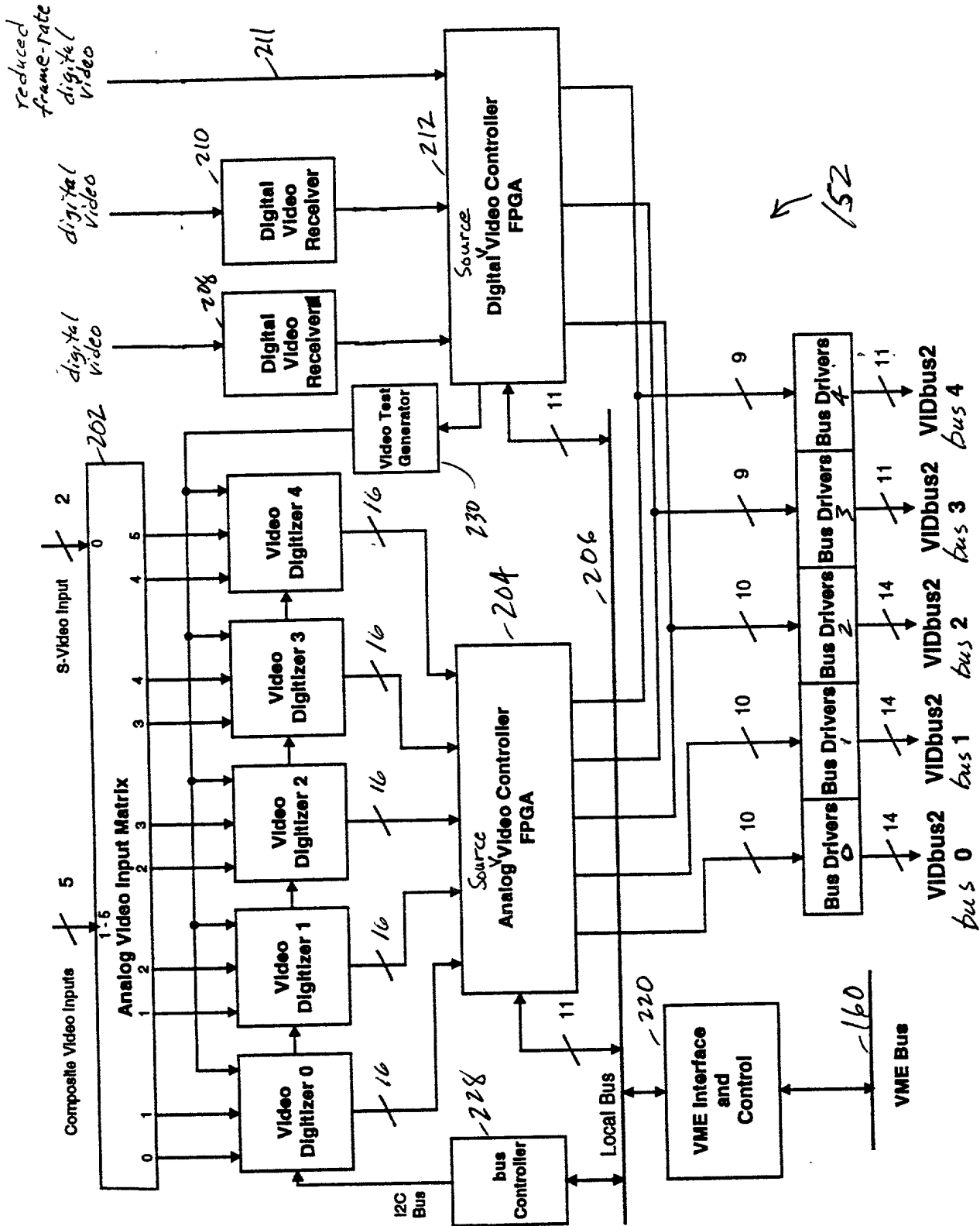


FIG. 3

The diagram illustrates the system architecture with the following components and connections:

- fail-safe Video**: Provides a 164-bit input to the **Multiplexer and relay module**.
- terminal controllers**: Provide a 252-bit input to the **Multiplexer and relay module**.
- Multiplexer and relay module**: Receives inputs from the fail-safe video and terminal controllers. It outputs 254, 256, and 258-bit signals to the **RGB VIDEOS OUT**.
- VME INTERFACE AND CONTROL LOGIC**: A central control unit that interfaces with the multiplexer module and the VME bus.
 - It receives a 256-bit signal from the multiplexer module.
 - It sends a 271-bit signal back to the multiplexer module.
 - It has a 10-bit control line and an 8-bit data line connecting to the VME bus.
- VME Bus**: A 156-bit bus that carries control and data signals between the VME interface and the rest of the system.

FIG. 4

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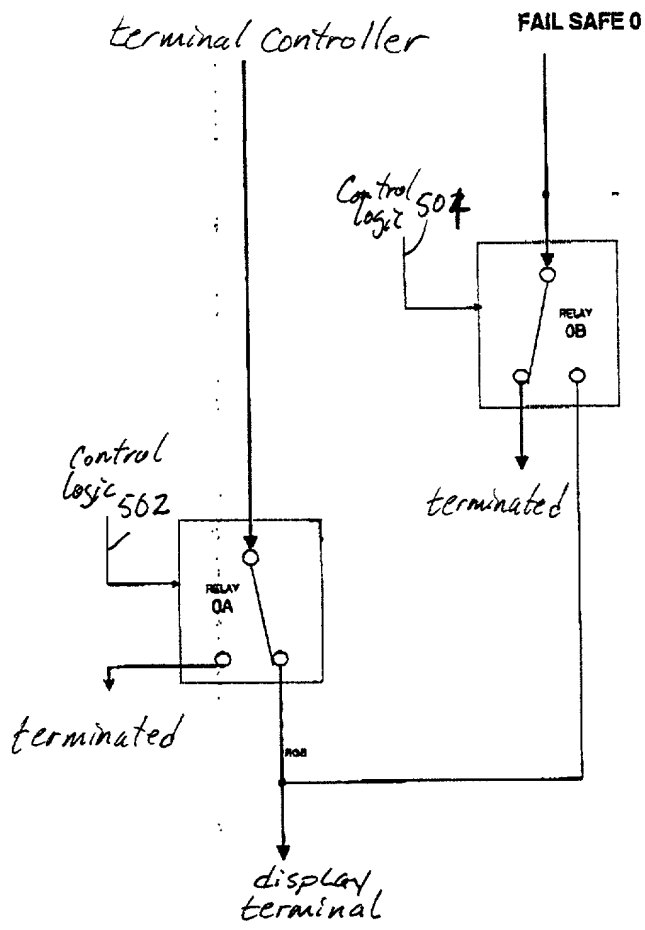


FIG. 5

Figure 1 is a block diagram of a fail-safe control system for a four-station conveyor. The system includes a terminal controller, four fail-safe stations (1-4), and a common output line 622. Each station has a relay (1A, 2A, 3A, 4A) and a relay (1B, 2B, 3B, 4B). The stations are connected to a common output line 622 via a series of 8-to-1 multiplexers (601, 602, 603, 604). The diagram shows the flow of signals from the terminal controller through the stations and multiplexers to the output line 622.

FIG. 6A

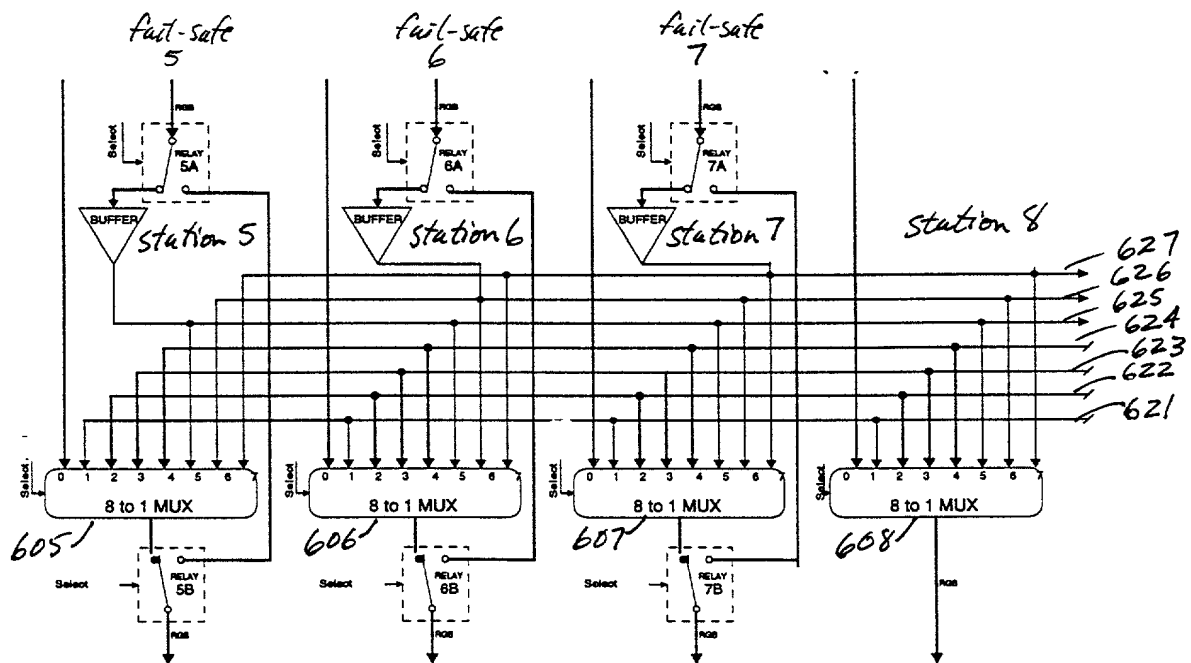


FIG. 6B